



The IsoLoop™ Advantage

Single Channel Devices.

Model Number	Manufacturer	Data Rate (10%PVD) see Note 1.	Prop. Delay (max)	P.W.D. (max) $t_{PHL}-t_{PLH}$	Prop. Delay Skew	Trans. Immunity	I_{supply} @ 10Mb	Temp. Range °C
IL710 **	NVE	100Mb (33Mb)	15ns	3ns	4ns	20kV/us	5.5mA	-40 to +100
HCPL-0721 **	HP	25Mb (16Mb)	40ns	6ns	20ns	10kV/us	10mA	-40 to +85
HCPL-0710 **	HP	12.5Mb(12.5Mb)	40ns	8ns	20ns	10kV/us	10mA	-40 to +100
HCPL-0611	HP	10Mb (3Mb)	100ns	35ns	40ns	10kV/us	10mA	-40 to +85
TLP2601	Toshiba	10Mb	75ns	not quoted	not quoted	10kV/us	10mA	0 to +70
PS9601	NEC	10Mb	75ns	not quoted	not quoted	not quoted	10mA	0 to +70
740L6000 **	QT	10Mb	100ns	not quoted	not quoted	15kV/us	10mA	0 to +70

Dual Channel Devices.

Model Number	Manufacturer	Data Rate (10%PVD) see Note 1.	Prop. Delay (max)	P.W.D. (max) $t_{PHL}-t_{PLH}$	Prop. Delay Skew	Trans. Immunity	I_{supply} @ 10Mb	Temp. Range °C
IL711 **	NVE	100Mb (33Mb)	15ns	3ns	4ns	20kV/us	10mA	-40 to +100
IL712 **	NVE	100Mb (33Mb)	15ns	3ns	4ns	20kV/us	10mA	-40 to +100
HCPL-2430	HP	20Mb (3Mb)	60ns	25ns	35ns	10kV/us	33mA	0 to +70
TLP2630	Toshiba	10Mb	75ns	not quoted	not quoted	200V/us	24mA	0 to +70

4-Channel Devices.

Model Number	Manufacturer	Data Rate (10%PVD) see Note 1.	Prop. Delay (max)	P.W.D. (max) $t_{PHL}-t_{PLH}$	Prop. Delay Skew	Trans. Immunity	I_{supply} @ 10Mb	Temp. Range °C
IL715 **	NVE	100Mb (33Mb)	15ns	3ns	4ns	20kV/us	5.5mA	-40 to +100
IL716 **	NVE	100Mb (33Mb)	15ns	3ns	4ns	20kV/us	5.5mA	-40 to +100
IL717 **	NVE	100Mb (33Mb)	15ns	3ns	4ns	20kV/us	5.5mA	-40 to +100

Note 1. Maximum data rate at 10% pulse width distortion = $(PVD \times 10)^{-1}$

** True logic to logic devices. No external components required.